

IEEE Workshop on Design and Diagnostics  
of Electronic Circuits and Systems

Steering Committee  
awards

# Jan Hlavíčka Prize

to the best paper  
from the area of design

A Flexible Hardware Architecture  
for Fast Access  
on Large Non-Volatile Memories

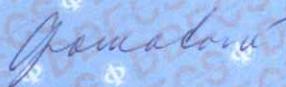
Eric Armengaud, Florian Rothensteiner, Andreas Steininger

Vienna University of Technology, Austria

Martin Horauer

University of Applied Sciences, Austria

presented on the 8<sup>th</sup> DDECS 2005,  
Sopron, Hungary



April 19, 2006, Prague, Czech Republic

