

Call for Papers

Austrochip is the traditional annual workshop on microelectronics and integrated circuits in Austria and neighboring countries. The 34th edition will be organized by the Department of Electronic Engineering & Entrepreneurship, Research Group Embedded Systems, FH Technikum Wien.

Topics of the workshop include but are not limited to

- Analog, mixed-signal, and RF integrated circuits
- Digital circuits, filters, DSPs, asynchronous designs
- FPGA design and reconfigurable hardware
- Design methodology, system-level design, giga scale circuits, network-on-chip
- Embedded systems & IoT, energy-efficient machine learning, low-power designs, RF systems, security aspects
- Verification and testing, signal integrity, device modeling, timing analysis, reliability simulation, EMC, ESD
- Emerging technologies, quantum computing, nano CMOS process, sub-threshold circuits, sensors, organic and biomedical electronics
- Case studies and prototyping

Submissions from academia as well as industry are encouraged. Submissions will be double-blind-reviewed. Papers accepted by the program committee will be made available electronically at the conference with the plan to publish all papers written in English through IEEE Xplore.

Instructions for Authors

Papers shall be in the range of 4-8 pages written in double-column IEEE A4 format. The only accepted file format is PDF. Submitted papers have to be anonymous (to allow a double-blind review) and have to be written in English.

Contact/Organization

Martin Horauer, FH Technikum Wien, Department of Electronics Engineering & Entrepreneurship, Research Group Embedded Systems

✉ martin.horauer@technikum-wien.at

🌐 <https://www.austrochip.at>

Important Dates

Submission Deadline

Aug. 03, 2026

Acceptance Notification

Sept. 21, 2026

Conference Date

Oct. 23, 2026

Conference Venue

FH Technikum Wien
Höchstädtplatz 6
A-1200 Wien

Organizer



Patrons & Sponsors