Contact

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Interests >> Partners

We are looking for R&D cooperation partners, e.g., in the fields of

- Automotive Electronics and Electromobility
- Traffic Management Systems
- Industrial Automation
- Home & Building Automation
- Ambient Assisted Living
- Smart Systems and Devices

We are looking for challenging

- Internships for *Bachelor-Students in Electronic Engineering*
- Diploma topics for *Master-Students in Embedded Systems*

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Expertise >> Reference Projects

Our team provides know-how and experiences in the following areas

- Software Design for Embedded Systems
- Hardware Design (PCB, FPGA, ASIC)
- Electronic System Level Design
- Distributed Systems, Real-Time Systems
- Design, Debugging, Test, and Verification
- Communication Networks and Fieldbusses
- Assistive Technologies

Reference Projects:

STEACS – Systematic Test of Embedded Automotive Communication Systems
In the context of this FIT-IT funded R&D project we developed test & diagnosis tools and concepts for remote testing of future automotive electronic systems based on FlexRay in cooperation with the OEM partners BMW and DaimlerChrysler.

Project Partners: Decomsys GmbH, ECS Group UT Vienna

DECS – Design Methods for Embedded Control Systems
This FHplus funded project adresses the topics of (i) formal specification and verification of embedded control system architectures and, (ii) elaborate test and diagnosis tools for distributed embedded systems with a focus on automotive communication systems.

Cooperations with Audi Venture Electronics GmbH, RWTH Aachen

Test und Diagnosis for FlexRay EPL Subsystems
The aim of this project is to identify various favourable bus cablings for FlexRay networks which are currently non-conformant to the FlexRay EPL specification. This is done by investigating bit error rates at the interface between the FlexRay commuication controller and the transceiver circuitry.

Project Partner: Gebauer & Griller Kabelwerke GmbH

SiTOS – Dynamic Route Information Panel Gateway
In this project a gateway for a traffic management system was developed that allows to display route-information via suitable information panels. The solution is based on a PC/104 platform running the SiTOS protocol.

Project Partner: Zelsiko GmbH

COORDES – Coordinated Test, Debugging, and Diagnosis in Distributed Embedded Systems
In the course of the research project COORDES (Research Studio Austria funding) a new, patented solution which enables coordinated test, debugging, and diagnosis in distributed embedded systems is realized using FPGA as well as ASIC prototyping.

Follow-up projects with Oregano Systems, hME, ...

AsTeRICS – Assistive Technology Rapid Integration and Construction Set
In the course of this international Framework 7 Collaborative Project funded by the European Commission, flexible and affordable setups for user driven Assistive Technologies dedicated to people with severely reduced motoric capabilities will be developed and explored.

Cooperation with 8 national / international partners